GX6207

High Output Current LDO Regulator , High PSRR , Low Dropout, GX6207 Se
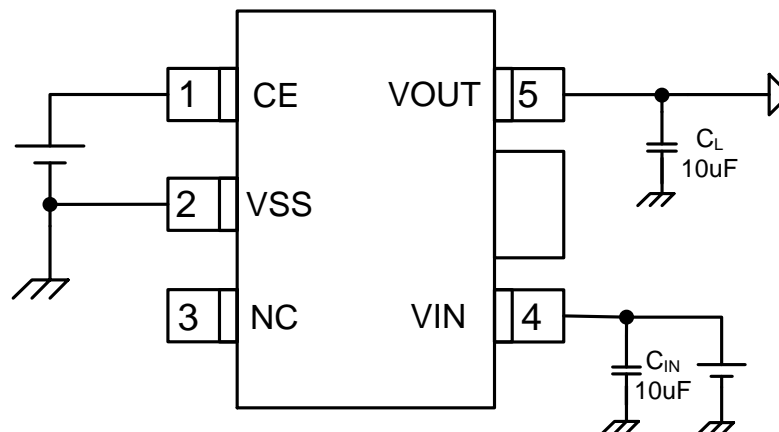
General Description

The GX6207 Series is a positive voltage regulator with a low dropout voltage,high output voltage accuracy,and low crrent consumption developed based on CMOS technology.

A built-in low on-resistance transistor provides a low dropout voltage and large output current, a built-in overcurrent protector prevents the load current from exceeding the current capacitance of the output transistor. An ON/OFF circuit ensures a long battery life. Compared with the voltage regulators using the conventional CMOS process, a larger variety of capacitors are available, including small ceramic capacitors.
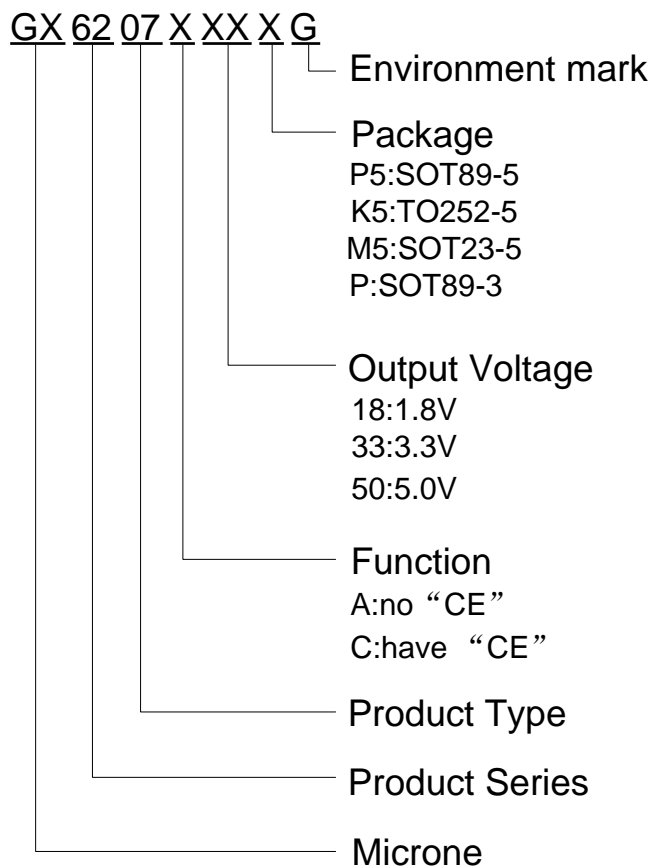
Features

- Maximum Output Current: 800 mA（$V_{IN} \geq V_{OUT}(T)+1.0V$）
- Dropout Voltage: 100mV@ I_{OUT} =300mA,V_{OUT} =5.0V
- Operating Voltage Range: 2V～6.5V
- Highly Accuracy: ±1％
- Low Current Consumption:
  During Operation: 82uA（TYP.）
  During Shutdown:0.1uA（TYP.）
- High Ripple Rejection: 65dB@1KHz (GX6207C50)
- Line Regulation: 0.05％（TYP.）

Typical Application

- Power supply for DVD and CD-ROM drives
- Power supply for personal communication device
- Power supply for battery-powered devices
- Power supply for note PCs

Package

- 3-pin SOT89-3
- 5-pin SOT89-5,SOT23-5,TO252-5

Typical Application Circuit

Selection Guide

GX 62 07 X XX X G

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GX ──────────────────────── Microne
   62 ───────────────────── Product Series
      07 ────────────────── Product Type
         X ───────────────── Function
                              A:no "CE"
                              C:have "CE"
           XX ────────────── Output Voltage
                              18:1.8V
                              33:3.3V
                              50:5.0V
              X ──────────── Package
                              P5:SOT89-5
                              K5:TO252-5
                              M5:SOT23-5
                              P:SOT89-3
                G ─────────── Environment mark
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product series	product description
GX6207A50PG	V_{OUT} =5.0V；no enable function；Package：SOT89-3
GX6207C18M5G	V_{OUT} =1.8V；have enable function；Package：SOT23-5
GX6207C33M5G	V_{OUT} =3.3V；have enable function；Package：SOT23-5
GX6207C50K5G	V_{OUT} =5.0V；have enable function；Package：TO252-5
GX6207C50M5G	V_{OUT} =5.0V；have enable function；Package：SOT23-5
GX6207C50P5G	V_{OUT} =5.0V；have enable function；Package：SOT89-5

NOTE: If you need other voltage and package, please contact our sales staff。
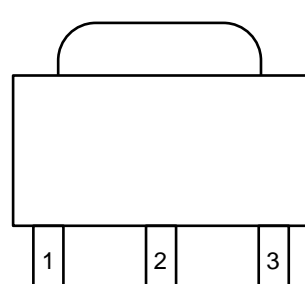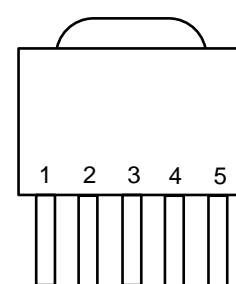
Pin Configuration& Pin Assignment
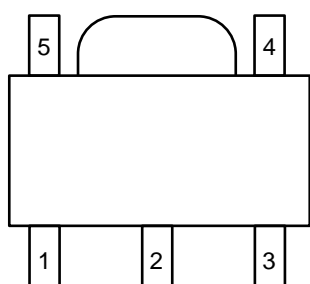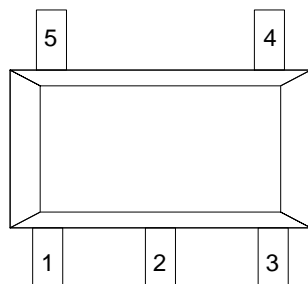
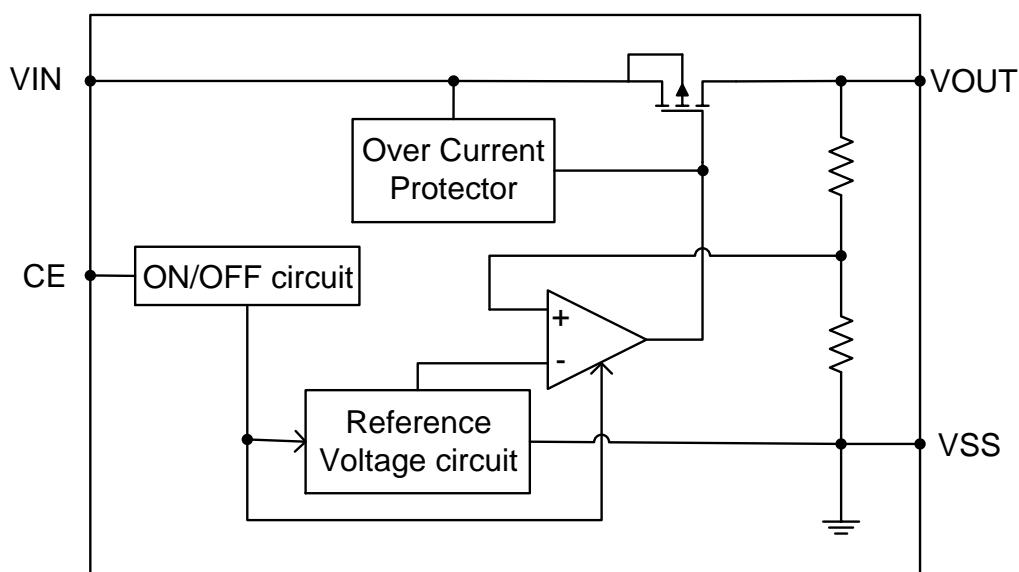SOT-89-5   SOT23-5   SOT89-3   TO252-5

Pin Assignment

GX6207AXX

Pin Number	Pin Name	Functions
SOT89-3		
1	V_{SS}	Ground
2	V_{IN}	Input Voltage
3	V_{OUT}	Output Voltage

GX6207CXX

Pin Number			Pin Name	Functions
SOT89-5	SOT23-5	TO252-5		
1	3	5	CE	ON / OFF Control
2	2	3	V_{SS}	Ground
3	4	1	NC	No Connect
4	1	2	V_{IN}	Input Voltage
5	5	4	V_{OUT}	Output Voltage

Block Diagram

Absolute Maximum Ratings

Parameter		Symbol	Ratings	Units
Input Voltage		V_{IN}	7.0	V
Output Current		I_{OUT}	800	mA
Output Voltage		V_{OUT}	Vss-0.3～V_{IN} +0.3	V
CE Pin Voltage		V_{CE}	Vss-0.3～V_{IN} +0.3	V
Power Dissipation	SOT-89-5	P_D	1000	mW
	TO252-5		1000	mW
	SOT-23-5		250	mW
	SOT-89-3		500	mW
Operating Temperature Range		T_{OPR}	−40～+85	℃
Storage Temperature Range		T_{STG}	−40～+125	℃

Electrical Characteristics

(V_{IN}= V_{OUT} (T) +1.0V, V_{CE} = V_{IN}, $C_{IN=}C_L$=10uF, Ta=25°C ,unless otherwise noted)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Units
Output Voltage	V_{OUT}(E) (Note 2)	I_{OUT}=100mA		X0.99	V_{OUT} (T) (Note 1)	X1.01	V
Input voltage	V_{IN}	-		2.0	-	6.5	V
Maximum Output Current	I_{OUTMAX} (Note 4)	V_{IN}≥ V_{OUT} (T) +1.0V		-	800	-	mA
Load Regulation	ΔV_{OUT}	1mA≤I_{OUT}≤300mA		-	10	100	mV
Line Regulation	$\dfrac{\Delta V_{OUT}}{\Delta V_{IN} \bullet V_{OUT}}$	I_{OUT} =100mA V_{OUT} (T) +0.5V ≤V_{IN}≤6.5V		-	0.05	0.3	%/V
Dropout Voltage	V_{DIF} (Note 3)	I_{OUT} =300mA	1.8V≤V_{OUT} (T)≤2.0V	-	200	260	mV
			3.0V≤V_{OUT} (T)≤5.5V	-	100	180	
Current consumption during operation	I_{SS1}	CE pin = ON, no load		-	82	160	μA
Current consumption during shutdown	I_{SS2}	CE pin = OFF, no load		-	0.1	1.0	μA
CE "High" Voltage	V_{CEH}	Start up,R_L=1.0KΩ		1.5	-	-	V
CE "Low" Voltage	V_{CEL}	Shut down, R_L=1.0KΩ		-	-	0.3	V
CE "High" Current	I_{CEH}	V_{IN}=6.5V,V_{CE}=6.5V		-0.1	-	0.1	μA
CE "Low" Current	I_{CEL}	V_{IN}=6.5V,V_{CE}=0V		-0.1	-	0.1	μA
Ripple Rejection Rate	|RR|	V_{IN}=6V , △Vrip=0.5Vrms, I_{OUT}=100mA, f=1kHz	1.8V≤V_{OUT}, (T)≤2.0V	-	70	-	dB
			3.0V≤V_{OUT}, (T)≤5.5V	-	65	-	

Short-circuit current	I$_{short}$	CE pin = ON, VOUT = 0 V	-	350	-	mA

Note ：

1. V$_{OUT}$ (T) ：Specified Output Voltage

2. V$_{OUT}$ (E) ：Effective Output Voltage (Ie. The output voltage when "V$_{OUT}$ (T)+1.0V"is provided at the Vin pin while maintaining a certain Iout value.)

3. V$_{DIF}$：V$_{IN1}$ −V$_{OUT}$ (E)'

V$_{IN1}$ ：The input voltage when V$_{OUT}$(E)' appears as input voltage is gradually decreased.

V$_{OUT}$ (E)'=A voltage equal to 98% of the output voltage whenever an amply stabilized Iout　{V$_{OUT}$ (T)+1.0V} is input.

4. I$_{OUTMAX}$：Due to restrictions on the package power dissipation, this value may not be satisfied. Attention should be paid to the power dissipation of the package when the output current is large.This specification is guaranteed by design.

Operation

1. Basic operation

Figure 1 shows the block diagram of the GX6207 Series.

The error amplifier compares the reference voltage (Vref) with Vfb, which is the output voltage resistance-divided by feedback resistors Rs and Rf. It supplies the output transistor with the gate voltage necessary to ensure a certain output voltage free of any fluctuations of input voltage and temperature.
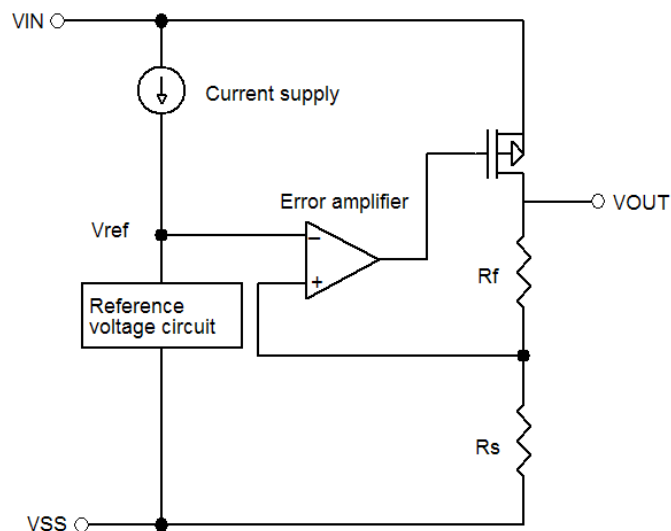

Figure　1

2. Output transistor

The GX6207 Series uses a low on-resistance P-channel MOS FET as the output transistor.Be sure that VOUT does not exceed VIN + 0.3 V to prevent the voltage regulator from being damaged due to inverse current flowing from VOUT pin through a parasitic diode to VIN pin.

3. Shutdown pin (CE pin)

This pin starts and stops the regulator.

When the CE pin is set to the shutdown level, the operation of all internal circuits stops, and the built-in P-channel MOS FET output transistor between the VIN pin and VOUT pin is turned off to substantially reduce the current consumption. The VOUT pin becomes the VSS level due to the internally divided resistance of several hundreds kΩ between the VOUT pin and VSS pin. Since the CE pin is neither pulled down nor pulled up internally, do not use it in the floating state.   In addition, note that the current consumption increases if a voltage of 0.3 V to VIN − 0.3 V is applied to the CE pin.

Table 1

CE Pin	Internal Circuits	VOUT Pin Voltage	Current Consumption
"H" Power on	Operating	Set value	I_{SS1}
"L" Power off	Stopped	Vss level	I_{SS2}

Precautions

1. Wiring patterns for the VIN, VOUT and GND pins   should be designed so that the impedance is low.When mounting an output capacitor between the VOUT and VSS pins (CL) and a capacitor for stabilizing the input between VIN and VSS pins (CIN), the distance from the capacitors to these pins should be as short as possible.

2. Note that the output voltage may increase when a series regulator is used at low load current (1.0 mA or less).

3. Generally a series regulator may cause oscillation, depending on the selection of external parts. The following conditions are recommended for this IC. However, be sure to perform sufficient evaluation under the actual usage conditions for selection, including evaluation of temperature characteristics.

       Input capacitor (CIN): 4.7 µF or more

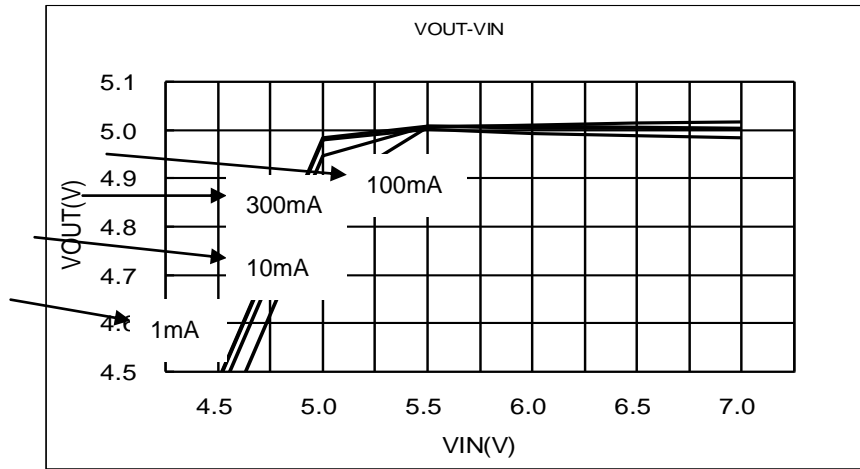   Output capacitor (CL): 4.7 µF or more

       Equivalent series resistance (ESR): 0.5 Ω or less

4. The voltage regulator may oscillate when the impedance of the power supply is high and the input capacitor is small or an input capacitor is not connected.

5. The application conditions for the input voltage, output voltage, and load current should not exceed the package power dissipation.

6. Do not apply an electrostatic discharge to this IC   that exceeds the performance ratings of the built-in electrostatic protection circuit.

7. SII claims no responsibility for any   disputes arising out of or in   connection with any infringement by products including this IC of patents owned by a third party.
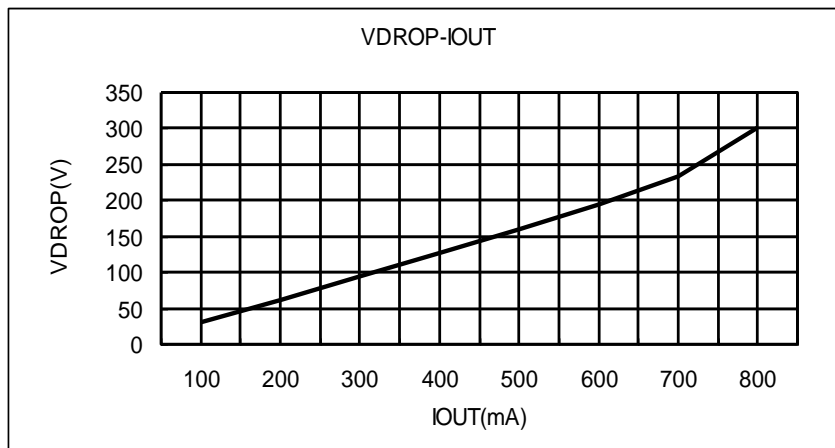
Type Characteristics

（1） Input VoltageVS.Output Voltage（Ta = 25 ℃）
GX6207C50P5G

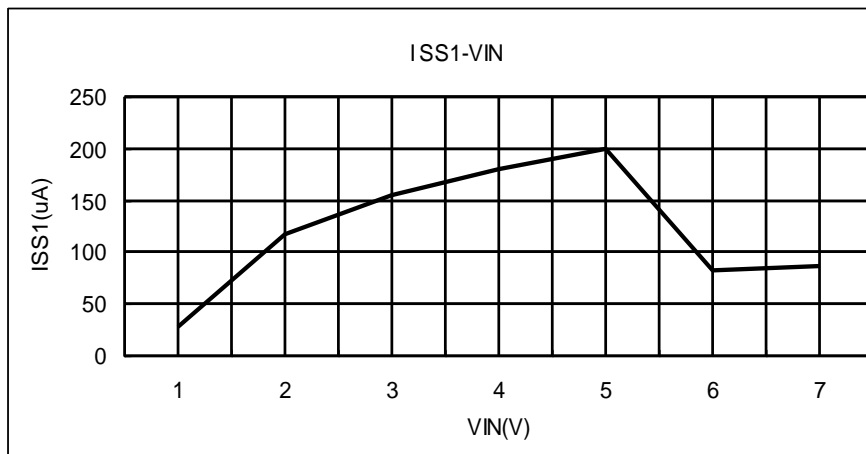（2） Output Current VS.Droput Voltage（$V_{IN}= V_{OUT}$ (T) +1.0V,Ta = 25 ℃）
GX6207C50P5G

（3） Input Voltage VS. Current Consumption（Ta = 25 ℃）
GX6207C50P5G

（4） Input VoltageVS.Output Voltage（Ta = 25 °C）
GX6207C33M5G

Vin VS. Vout
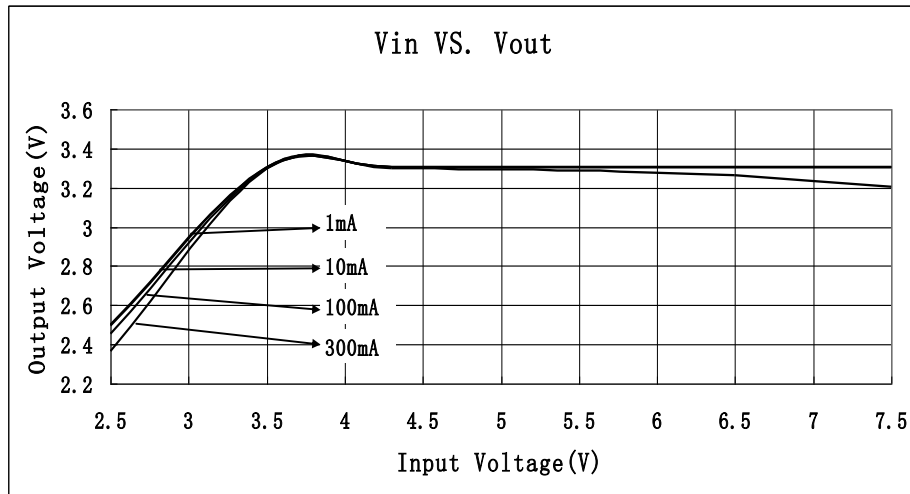
（5） Output Current VS.Droput Voltage（$V_{IN}= V_{OUT}$ (T) +1.0V,Ta = 25 °C）
GX6207C33M5G

Vdrop VS Iout
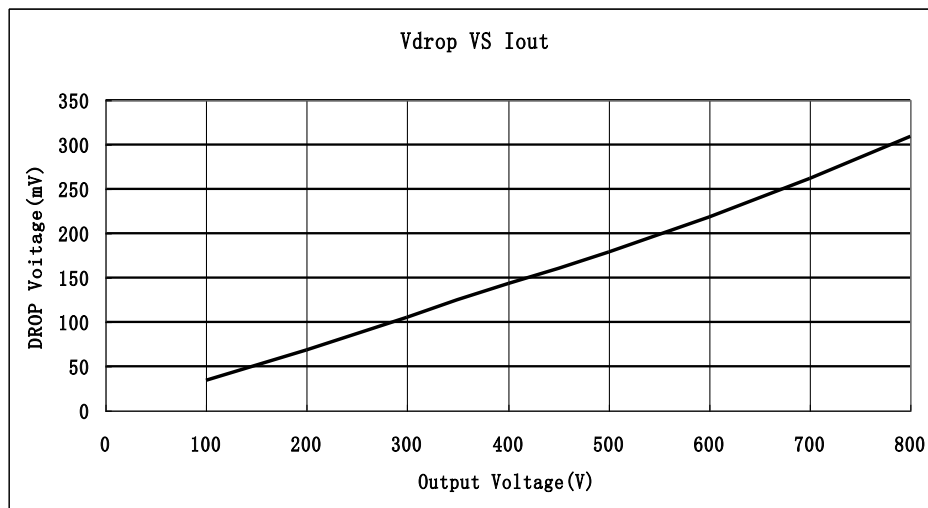
（6） Output Current VS. Current Consumption（$V_{IN}= V_{OUT}$ (T) +1.0V,Ta = 25 °C）
GX6207C33M5G

Vin VS Iss
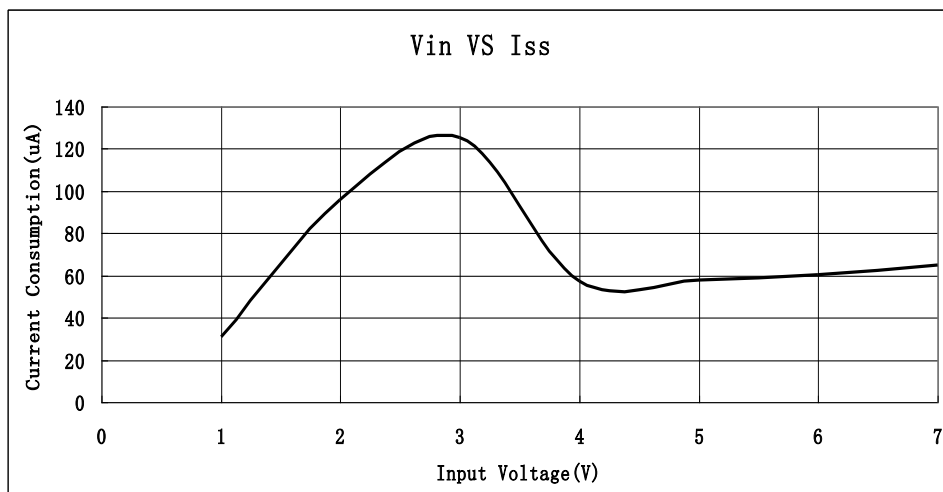
（7） Output Current VS. Output Voltage（$V_{IN}= V_{OUT}$ (T) +1.0V,Ta = 25 °C）
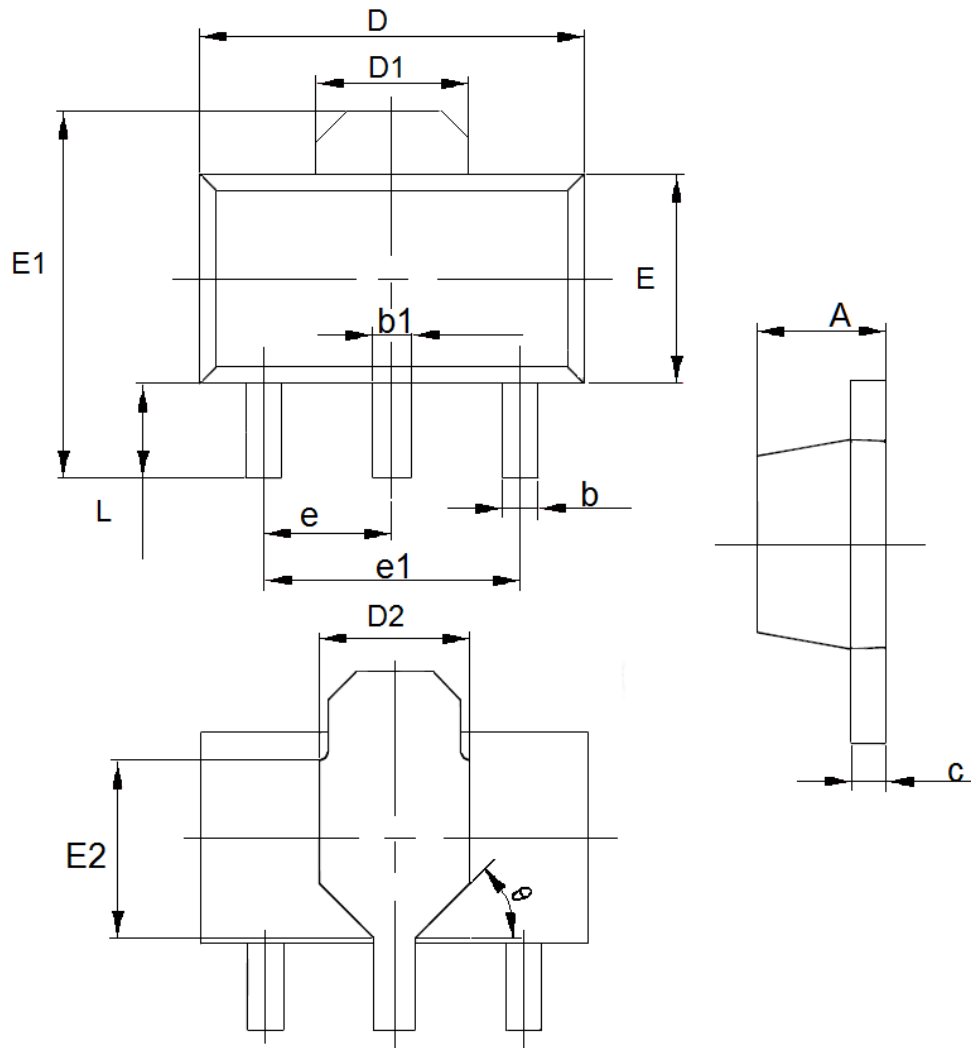
GX6207C33M5G
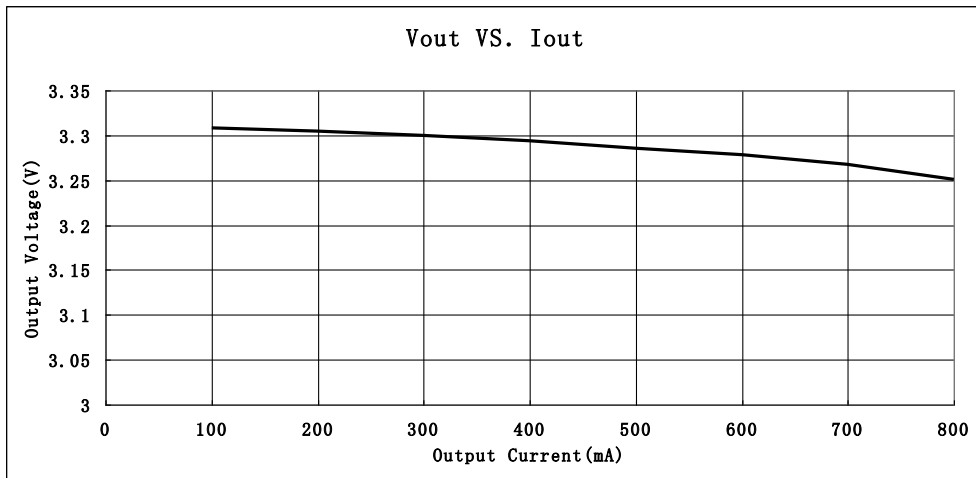
Package Information

- **SOT89-5**
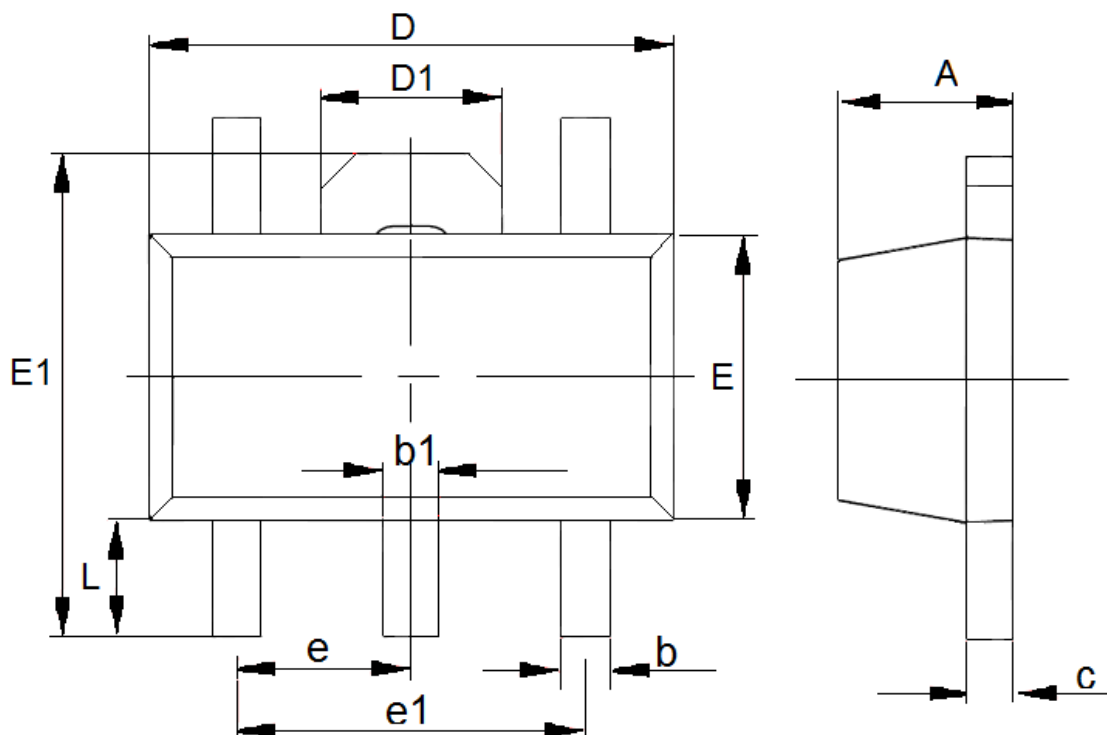
DIM	Millimeters		Inches	
	Min	**Max**	**Min**	**Max**
A	1.4	1.6	0.0551	0.0630
b	0.32	0.52	0.0126	0.0205
b1	0.38	0.58	0.0150	0.0228
c	0.35	0.47	0.0138	0.0185
D	4.4	4.6	0.1732	0.1811
D1	1.55(TYP)		0.061(TYP)	
e1	3(TYP)		0.1181(TYP)	
E	2.3	2.6	0.0906	0.1023
E1	3.94	4.4	0.1551	0.1732
e	1.5(TYP)		0.0591(TYP)	
L	0.8	1.2	0.0315	0.0472

- **SOT23-5**
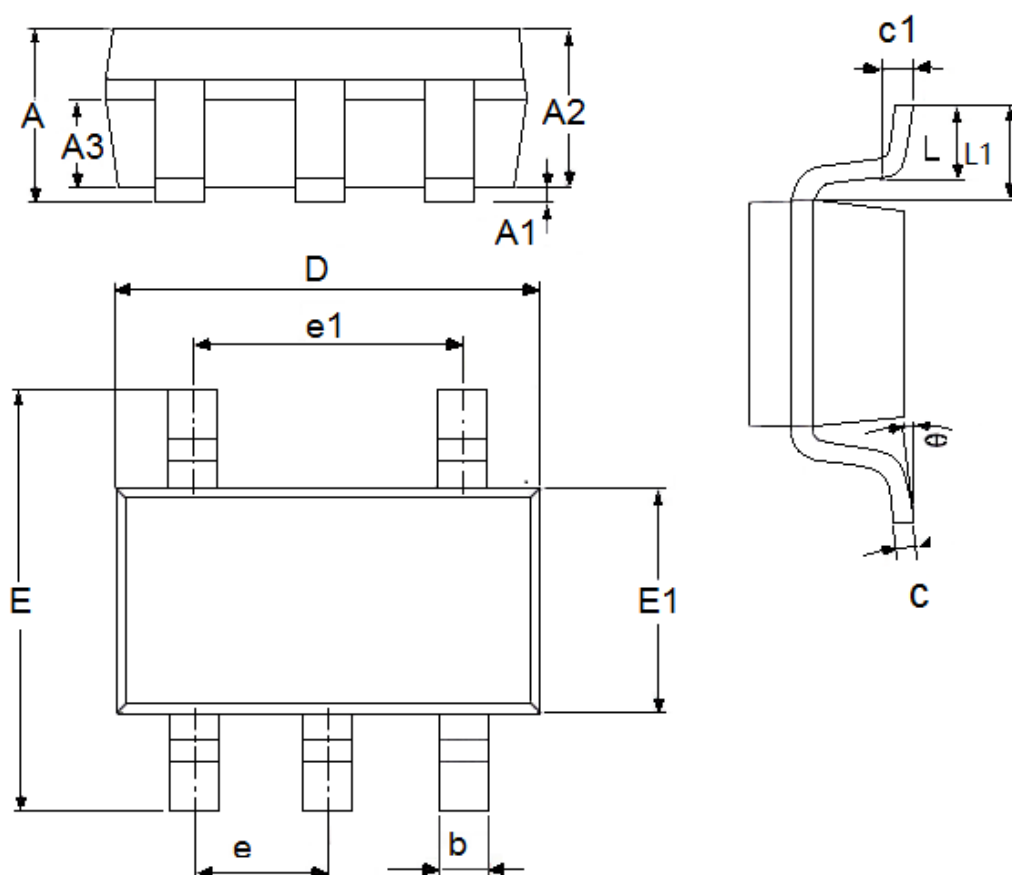
DIM	Millimeters		Inches	
	Min	**Max**	**Min**	**Max**
A	1.05	1.45	0.0413	0.0571
A1	0	0.15	0.0000	0.0059
A2	0.9	1.3	0.0354	0.0512
A3	0.6	0.7	0.0236	0.0276
b	0.25	0.5	0.0098	0.0197
c	0.1	0.23	0.0039	0.0091
D	2.82	3.05	0.1110	0.1201
e1	1.9(TYP)		0.0748(TYP)	
E	2.6	3.05	0.1024	0.1201
E1	1.5	1.75	0.0512	0.0689
e	0.95(TYP)		0.0374(TYP)	
L	0.25	0.6	0.0098	0.0236
L1	0.59(TYP)		0.0232(TYP)	
θ	0	8°	0.0000	8°
c1	0.2(TYP)		0.0079(TYP)	

- **SOT89-3**

DIM	Millimeters		Inches	
	Min	**Max**	**Min**	**Max**
A	1.4	1.6	0.0551	0.0630
b	0.32	0.52	0.0126	0.0205
b1	0.4	0.58	0.0157	0.0228
c	0.35	0.45	0.0138	0.0177
D	4.4	4.6	0.1732	0.1811
D1	1.55(TYP)		0.061(TYP)	
D2	1.75(TYP)		0.0689(TYP)	
e1	3.0(TYP)		0.1181(TYP)	
E	2.3	2.6	0.0906	0.1023
E1	3.94	4.4	0.1551	0.1732
E2	1.9(TYP)		0.0748(TYP)	
e	1.5(TYP)		0.0591(TYP)	
L	0.8	1.2	0.0315	0.0472
θ	45°		45°	

- **TO252-5**
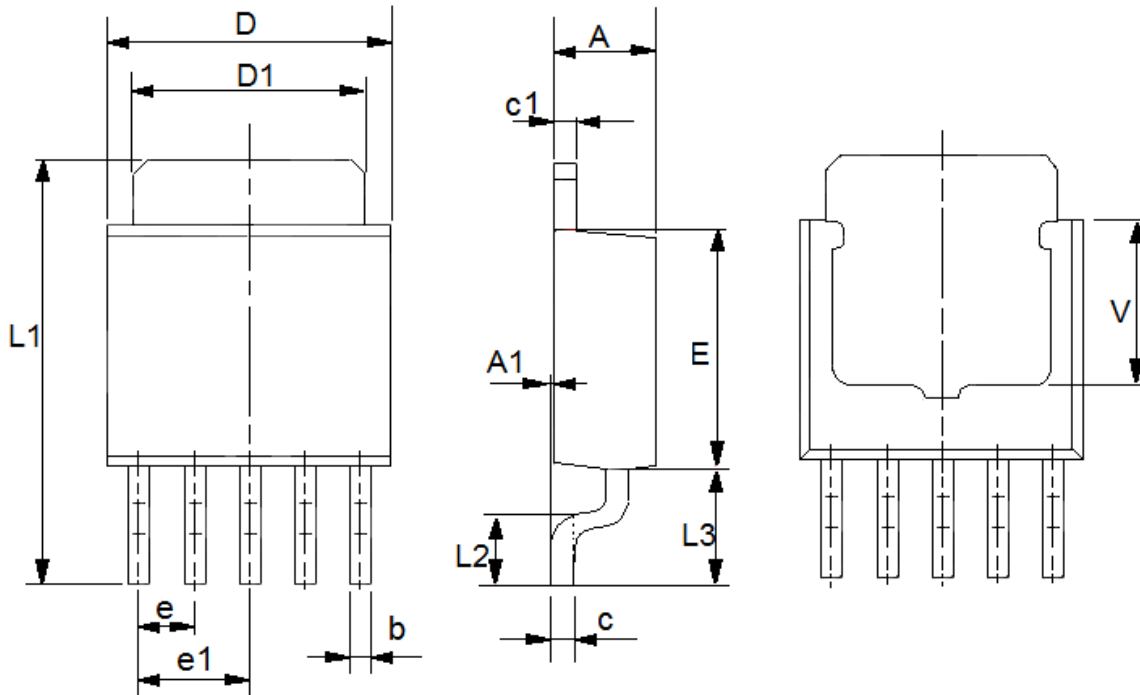
DIM	Millimeters		Inches	
	Min	**Max**	**Min**	**Max**
A	2.2	2.4	0.0866	0.0945
A1	0	0.127	0.0000	0.005
b	0.4	0.6	0.0157	0.0236
c	0.43	0.58	0.0169	0.0228
c1	0.43	0.58	0.0169	0.0228
D	6.3	6.7	0.2480	0.2638
D1	5.2	5.4	0.2047	0.2126
E	5.4	5.7	0.2126	0.2244
e	1.27(TYP)		0.05(TYP)	
e1	2.54(TYP)		0.1(TYP)	
L1	9.5	9.9	0.374	0.3898
L2	1.4	1.78	0.0551	0.0701
L3	2.55	2.9	0.1004	0.0701
V	3.45(TYP)		0.1358(TYP)	